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FOURTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known	
				Application Number	09/925,314
				Filing Date	August 10, 2001
				First Named Inventor	Christopher R. Risucci
				Art Unit	2183
				Examiner Name	Tsai, Henry
Sheet	1	of	1	Attorney Docket Number	1778.0180000

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
11/27	US1	5,522,086	05/1996	Burton <i>et al.</i>	
11/27	US2	6,631,460	10/2003	Morris <i>et al.</i>	
	US3				
	US4				
	US5				
	US6				
	US7				
	US8				
	US9				
	US10				
	US11				
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
	FP1					
	FP2					
	FP3					
	FP4					
	FP5					
	FP6					
	FP7					
	FP8					
	FP9					

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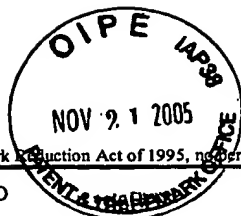
(MIPS 0106.00US)

Examiner Signature		Date Considered	04/18/06
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Substitute for form 1449/PTO

**FOURTH SUPPLEMENTAL
INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Attorney Docket Number	1778.0180000

Sheet 1 of 1

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume issue number(s), publisher, city and/or country where published	T ²
127	NPL1	"MIPS32 Architecture for Programmers Volume IV-a: The MIPS16e Application-Specific Extension to the MIPS32 Architecture," Document No. MD00076, Rev. 2.50, MIPS Technologies, Inc., Mountain View, California, 162 pages (July 1, 2005).	
127	NPL2	MIPS64 Architecture for Programmers Volume IV-a: The MIPS16e Application-Specific Extension to the MIPS64 Architecture, Document No. MD00077, Rev. 2.50, MIPS Technologies, Inc., Mountain View, California, 210 pages (July 1, 2005)	
	NPL3		
	NPL4		
	NPL5		
	NPL6		
	NPL7		
	NPL8		
	NPL9		
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(MIPS 0106.00US)

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¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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